

## JMPI(Ra,Lit,Rc) POUR ULG02

```
0  SMAR <- Ra
1  A <- SRAM
2  B <- Lit
3  DMAR <- A+B
4  DMAR <- DRAM
5  SMAR <- Rc
6  SRAM <- PC
7  A <- DRAM
8  B <- A+A; Latch CC
/C==0 9  PC <- A (SVC=1)
C==0   9  PC <- A (SVC=0)
10  DMAR <- PC; PC+
11  INSTREG <- DRAM
```

## JMPI(Ra,Lit,Rc) POUR ULG03 (MODE UTILISATEUR, IRQ=0 ET PC31=0)

```
0  SMAR <- Ra
1  A <- SRAM
2  B <- Lit
3  A <- A+B
```

```

4 B <- UDVP
5 OFF <- A
6 UDVP <- A
7 B <- A /XOR B
8 RMAR <- (-1)
9 A <- ROM
10 A <- A OR B; Latch CC
E==0 11 A <- (-1)
12 RMAR <- A-1
13 SMAR <- ROM
14 SRAM <- PC
15 A <- A-1
16 RMAR <- A-1
17 PC <- ROM; SVR
18 SPP <- PC
19 OFF <- PC; PC+
20 INSTREG
E==1 11 A <- DRAM
12 B <- UCVP
13 SMAR <- Rc
14 SRAM <- PC
15 OFF <- A
16 UCVP <- A

```

```
17  PC <- A
18  B <- A /XOR B
19  RMAR <- (-1)
20  A <- ROM
21  A <- A OR B
E==0 22  A <- (-1)
23  RMAR <- A-1
24  SMAR <- ROM
25  SRAM <- PC
26  A <- A-1
27  A <- A-1
28  RMAR <- A-1
29  PC <- ROM; SVR
30  OFF <- PC; PC+
31  INSTREG <- DRAM
E==1 21  OFF <- PC; PC+
22  INSTREG <- DRAM
```