Next instruction

USER	SVR
OFF ← PC ;PC+	SPP ← PC
INSTREG ← DRAM ;UC/D=0	OFF ← PC ;PC+
	INSTREG ← DRAM

Miss exceptions (<u>old conv.</u>) (A = -1 already)

CODE	DATA
A ← A-1	A ← A-1
A ← A-1	RMAR ← A-1
RMAR ← A-1	PC ← ROM ;SVR=1
PC ← ROM ;SVR=1	SPP ← PC
SPP ← PC	OFF ← PC ;PC+
OFF ← PC ;PC+	INSTREG ← DRAM
INSTREG ← DRAM	

Check virtual addr (<u>old conv.</u>)
(A = desired vaddr already)

B ← UxVP
UxVP ← A
B ← A NXOR B
RMAR ← -1
A ← ROM
A ← A OR B   ;Latch

Old conventions

ROM[-1] = offset mask

ROM[-2] = XP *(to load in SMAR)*

ROM[-3] = data miss handler

ROM[-4] = code miss handler

New conventions

ROM[-1] = XP *(to load in SMAR)*

ROM[-2] = data miss handler

ROM[-3] = code miss handler