

# BETA ASSEMBLY

Simulated execution

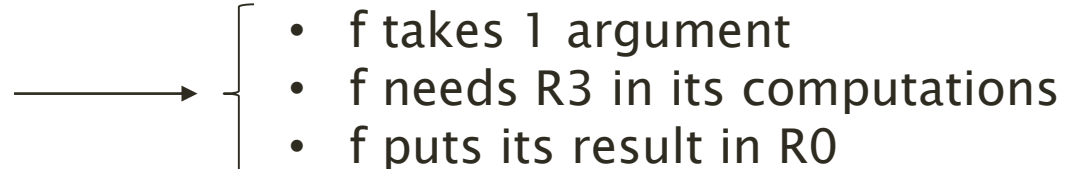
# A SIMPLE PROGRAM

```
1 .include beta.uasm
2     CMOVE(tos, SP)
3     BR(main)
4
5 x:
6     LONG(42)
7
8 f:
9     PUSH(LP)
10    PUSH(BP)
11    MOVE(SP, BP)
12    PUSH(R3)
13    | do smtg, put result in R0
14    POP(R3)
15    POP(BP)
16    POP(LP)
17    RTN() | defined as JMP(LP, R31)
18
19 main:
20     LDR(x, R1)
21     PUSH(R1)
22     CALL(f) | defined as BR(f, R31)
23     DEALLOCATE(1)
24     HALT()
25
26 tos:
27
```

This program only calls a procedure « f » with argument « x ».

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```
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5 x:
6     LONG(42)
7
8 f:
9     PUSH(LP)
10    PUSH(BP)
11    MOVE(SP, BP)
12    PUSH(R3)
13    | do smtg, put result in R0
14    POP(R3)
15    POP(BP)
16    POP(LP)
17    RTN() | defined as JMP(LP, R31)
18
19 main:
20     LDR(x, R1)
21     PUSH(R1)
22     CALL(f) | defined as BR(f, R31)
23     DEALLOCATE(1)
24     HALT()
25
26 tos:
27
```

- 
- f takes 1 argument
  - f needs R3 in its computations
  - f puts its result in R0

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4
5 x:
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7
8 f:
9     PUSH(LP)
10    PUSH(BP)
11    MOVE(SP, BP)
12    PUSH(R3)
13    | do smtg, put result in R0
14    POP(R3)
15    POP(BP)
16    POP(LP)
17    RTN() | defined as JMP(LP, R31)
18
19 main:
20     LDR(x, R1)
21     PUSH(R1)
22     CALL(f) | defined as BR(f, R31)
23     DEALLOCATE(1)
24     HALT()
25
26 tos:
27
```

CMOVE(BP, SP) was omitted as  
it was not necessary

LP ??  
BP ?? SP ??

We don't care

32 bits

# LOADED IN MEMORY

```
1 .include beta.uasm
2   CMOVE(tos, SP)
3   BR(main)
4
5 x:
6   LONG(42)
7
8 f:
9   PUSH(LP)
10  PUSH(BP)
11  MOVE(SP, BP)
12  PUSH(R3)
13  | do smtg, put result in R0
14  POP(R3)
15  POP(BP)
16  POP(LP)
17  RTN() | defined as JMP(LP, R31)
18
19 main:
20   LDR(x, R1)
21   PUSH(R1)
22   CALL(f) | defined as BR(f, R31)
23   DEALLOCATE(1)
24   HALT()
25
26 tos:
27
```



PC →

	0	CMOVE(tos, SP)
	4	BR(main)
x:	8	42
f:	16	PUSH(LP)
	20	PUSH(BP)
	24	MOVE(SP, BP)
	28	PUSH(R3)
		...
	48	POP(R3)
	52	POP(BP)
	56	POP(LP)
	60	RTN()
main:	64	LDR(x, R1)
	68	PUSH(R1)
	72	CALL(f)
	76	DEALLOCATE(1)
	80	HALT()
tos:	84	0

BP ?? } We don't care  
 LP ?? }

PC →

	0	CMOVE (tos, SP)
	4	BR(main)
x:	8	42
f:	16	PUSH(LP)
	20	PUSH(BP)
	24	MOVE(SP, BP)
	28	PUSH(R3)
		...
	48	POP(R3)
	52	POP(BP)
	56	POP(LP)
	60	RTN()
main:	64	LDR(x, R1)
	68	PUSH(R1)
	72	CALL(f)
	76	DEALLOCATE(1)
	80	HALT()
SP → tos:	84	0

88	0
92	0
96	0
100	0
104	0
108	0
112	0
116	0
120	0
124	0
128	0
132	0
136	0
140	0
144	0
148	0
152	0
156	0

BP ?? } We don't care  
 LP ??

	0	CMOVE (tos, SP)
	4	BR (main)
x:	8	42
f:	16	PUSH (LP)
	20	PUSH (BP)
	24	MOVE (SP, BP)
	28	PUSH (R3)
		...
	48	POP (R3)
	52	POP (BP)
	56	POP (LP)
	60	RTN ()
main:	64	LDR (x, R1)
PC →	68	PUSH (R1)
	72	CALL (f)
	76	DEALLOCATE (1)
	80	HALT ()
SP →	tos:	84 0

88	0
92	0
96	0
100	0
104	0
108	0
112	0
116	0
120	0
124	0
128	0
132	0
136	0
140	0
144	0
148	0
152	0
156	0

BP ?? } We don't care  
 LP ??

	0	CMOVE (tos, SP)
	4	BR (main)
x:	8	42
f:	16	PUSH (LP)
	20	PUSH (BP)
	24	MOVE (SP, BP)
	28	PUSH (R3)
		...
	48	POP (R3)
	52	POP (BP)
	56	POP (LP)
	60	RTN ()
main:	64	LDR (x, R1)
	68	PUSH (R1)
PC →	72	CALL (f)
	76	DEALLOCATE (1)
	80	HALT ()
SP →	tos: 84	0

88	0
92	0
96	0
100	0
104	0
108	0
112	0
116	0
120	0
124	0
128	0
132	0
136	0
140	0
144	0
148	0
152	0
156	0

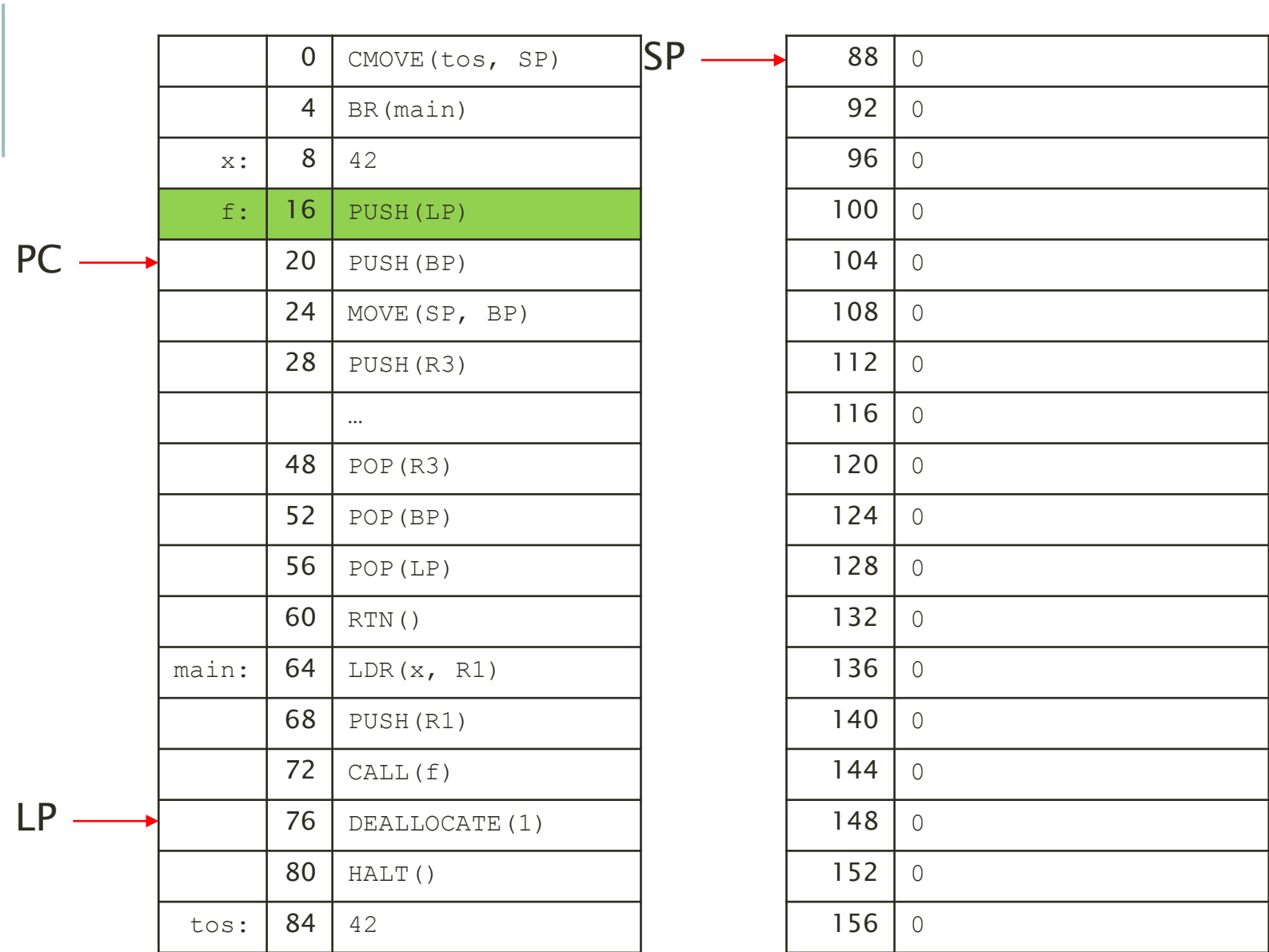


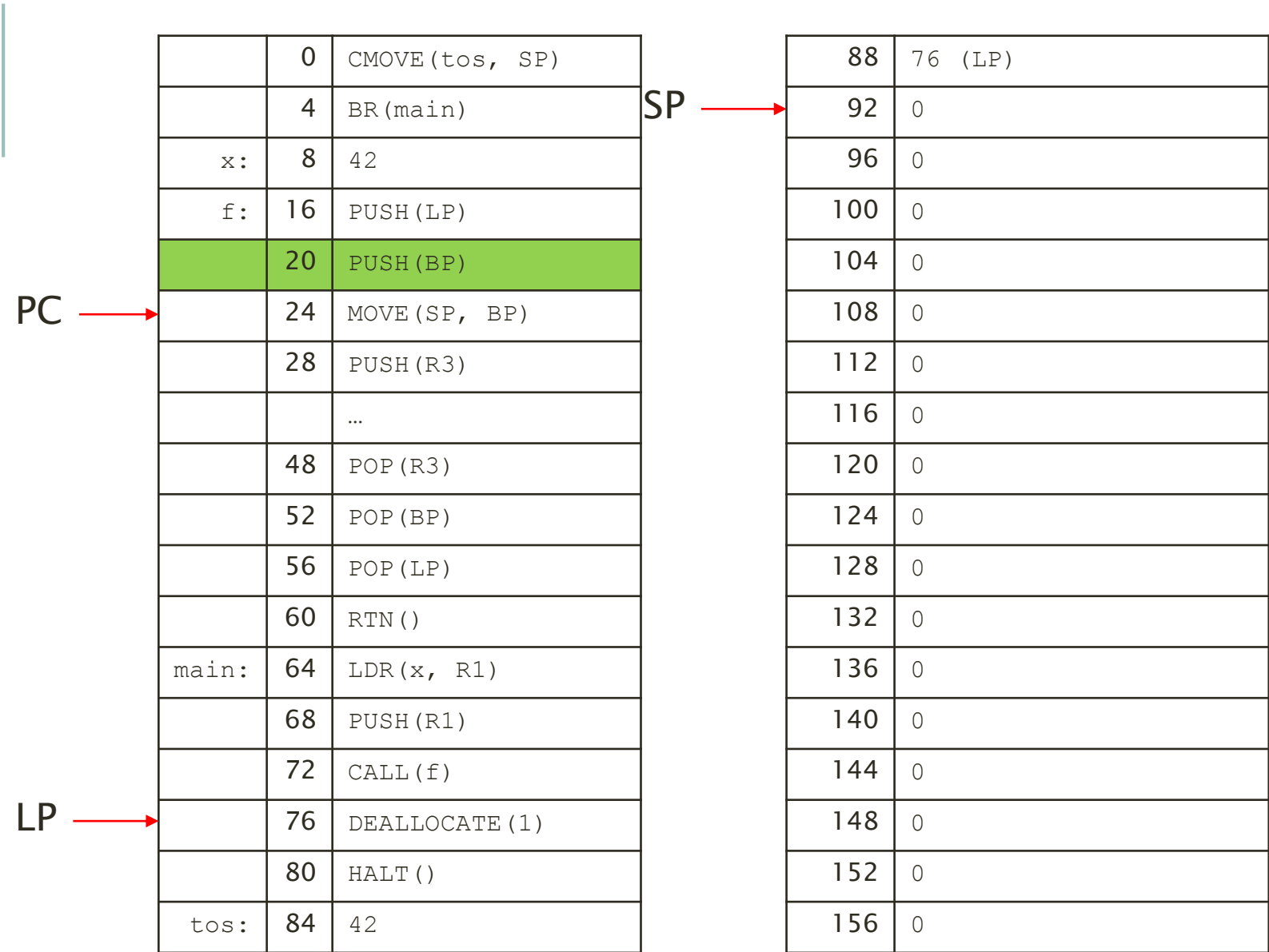
BP ?? } We don't care  
 LP ?? }

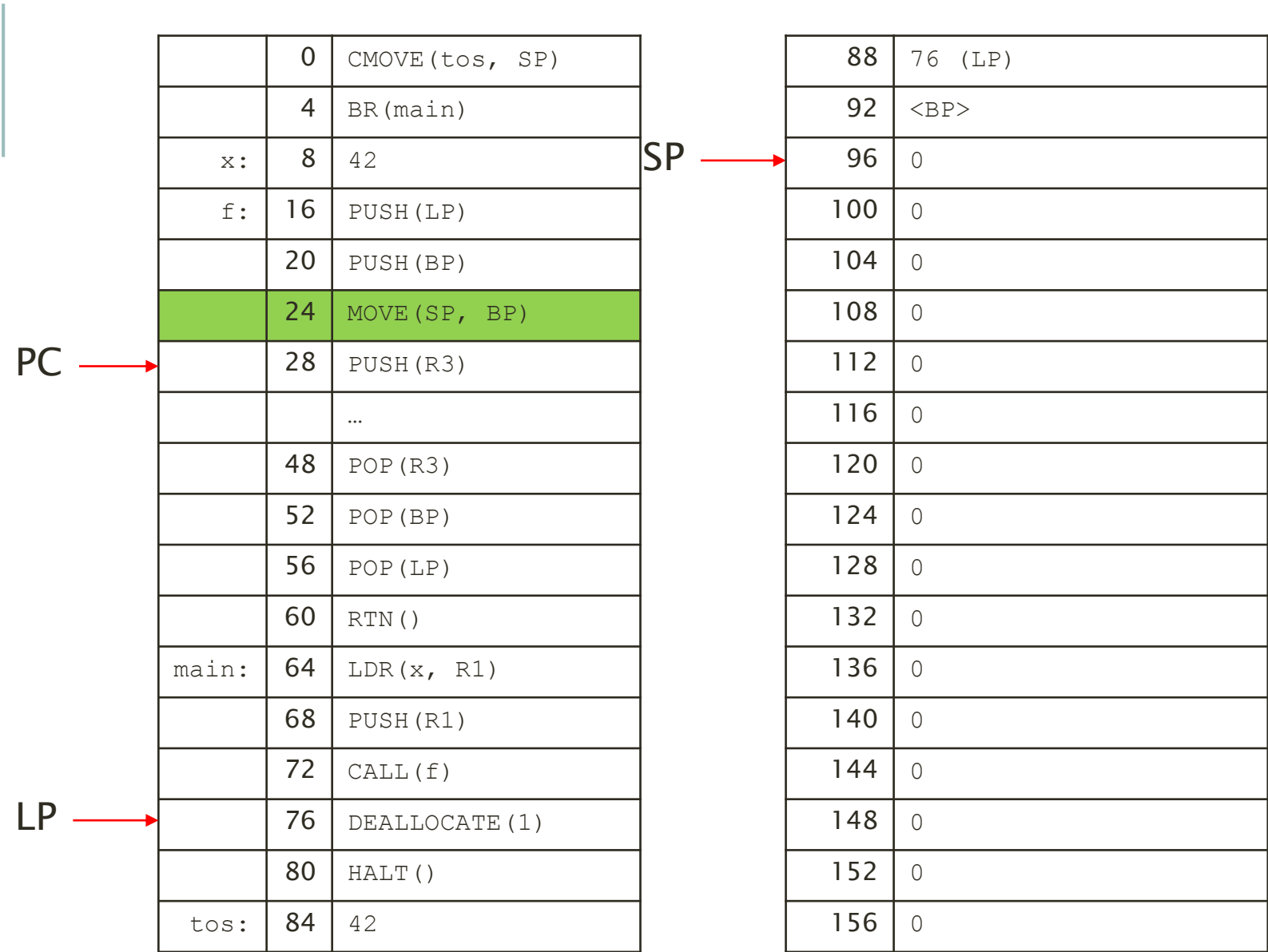
	0	CMOVE (tos, SP)	88	0
	4	BR (main)	92	0
x:	8	42	96	0
f:	16	PUSH (LP)	100	0
	20	PUSH (BP)	104	0
	24	MOVE (SP, BP)	108	0
	28	PUSH (R3)	112	0
		...	116	0
	48	POP (R3)	120	0
	52	POP (BP)	124	0
	56	POP (LP)	128	0
	60	RTN ()	132	0
main:	64	LDR (x, R1)	136	0
	68	PUSH (R1)	140	0
	72	CALL (f)	144	0
	76	DEALLOCATE (1)	148	0
	80	HALT ()	152	0
tos:	84	42	156	0

PC →

SP →





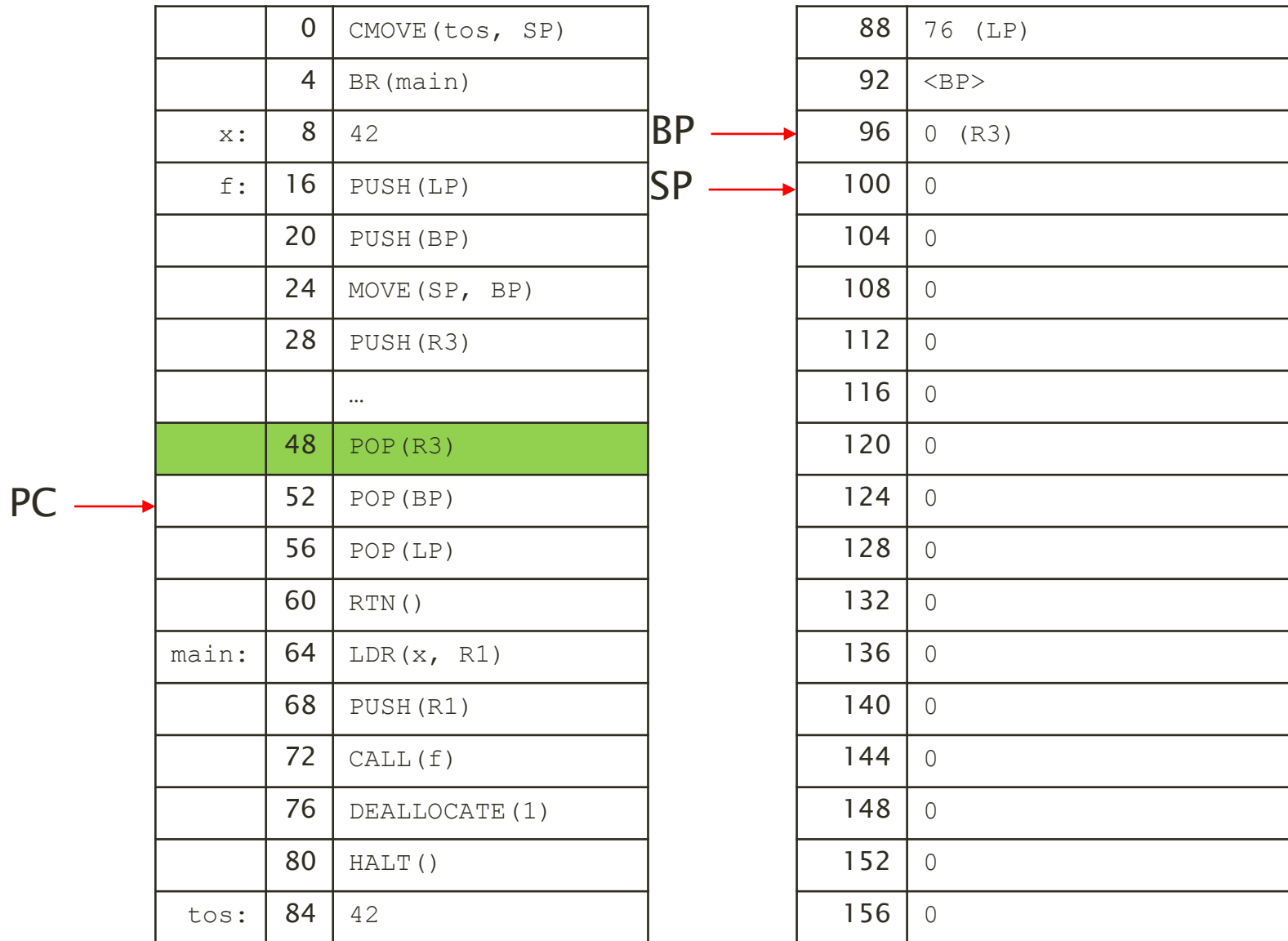


	0	CMOVE (tos, SP)
	4	BR(main)
x:	8	42
f:	16	PUSH(LP)
	20	PUSH(BP)
	24	MOVE(SP, BP)
	28	PUSH(R3)
PC →		...
	48	POP(R3)
	52	POP(BP)
	56	POP(LP)
	60	RTN()
main:	64	LDR(x, R1)
	68	PUSH(R1)
	72	CALL(f)
LP →	76	DEALLOCATE(1)
	80	HALT()
tos:	84	42

BP,SP →

	88	76 (LP)
	92	<BP>
	96	0
	100	0
	104	0
	108	0
	112	0
	116	0
	120	0
	124	0
	128	0
	132	0
	136	0
	140	0
	144	0
	148	0
	152	0
	156	0

LP??



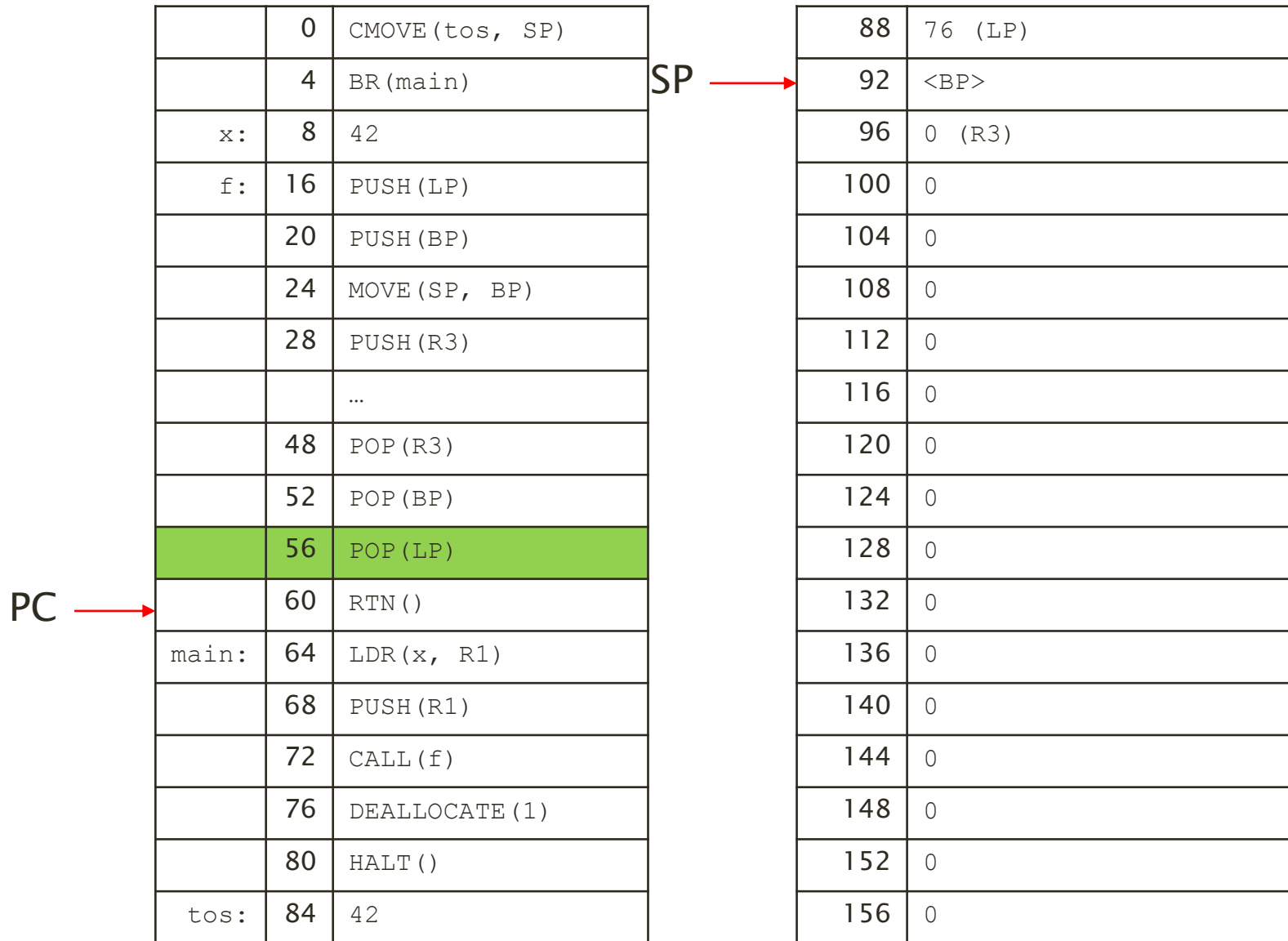
LP??

	0	CMOVE (tos, SP)
	4	BR(main)
x:	8	42
f:	16	PUSH(LP)
	20	PUSH(BP)
	24	MOVE(SP, BP)
	28	PUSH(R3)
		...
	48	POP(R3)
	52	POP(BP)
PC →	56	POP(LP)
	60	RTN()
main:	64	LDR(x, R1)
	68	PUSH(R1)
	72	CALL(f)
	76	DEALLOCATE(1)
	80	HALT()
tos:	84	42

BP,SP →

88	76 (LP)
92	<BP>
96	0 (R3)
100	0
104	0
108	0
112	0
116	0
120	0
124	0
128	0
132	0
136	0
140	0
144	0
148	0
152	0
156	0

BP = <BP>





	0	CMOVE (tos, SP)
	4	BR(main)
x:	8	42
f:	16	PUSH(LP)
	20	PUSH(BP)
	24	MOVE(SP, BP)
	28	PUSH(R3)
		...
	48	POP(R3)
	52	POP(BP)
	56	POP(LP)
	60	RTN()
PC →	main: 64	LDR(x, R1)
	68	PUSH(R1)
	72	CALL(f)
LP →	76	DEALLOCATE(1)
	80	HALT()
tos:	84	42

SP →

88	76 (LP)
92	<BP>
96	0 (R3)
100	0
104	0
108	0
112	0
116	0
120	0
124	0
128	0
132	0
136	0
140	0
144	0
148	0
152	0
156	0

	0	CMOVE (tos, SP)
	4	BR (main)
x:	8	42
f:	16	PUSH (LP)
	20	PUSH (BP)
	24	MOVE (SP, BP)
	28	PUSH (R3)
		...
	48	POP (R3)
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	56	POP (LP)
	60	RTN ()
main:	64	LDR (x, R1)
	68	PUSH (R1)
	72	CALL (f)
	76	DEALLOCATE (1)
	80	HALT ()
tos:	84	42

	88	76 (LP)
	92	<BP>
	96	0 (R3)
	100	0
	104	0
	108	0
	112	0
	116	0
	120	0
	124	0
	128	0
	132	0
	136	0
	140	0
	144	0
	148	0
	152	0
	156	0

LP →  
PC →

SP →

	0	CMOVE (tos, SP)
	4	BR(main)
x:	8	42
f:	16	PUSH(LP)
	20	PUSH(BP)
	24	MOVE(SP, BP)
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	48	POP(R3)
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	56	POP(LP)
	60	RTN()
main:	64	LDR(x, R1)
	68	PUSH(R1)
	72	CALL(f)
LP →	76	DEALLOCATE(1)
	80	HALT()
PC,SP →	tos: 84	42

88	76 (LP)
92	<BP>
96	0 (R3)
100	0
104	0
108	0
112	0
116	0
120	0
124	0
128	0
132	0
136	0
140	0
144	0
148	0
152	0
156	0